

Fabrication of Nb-based superconducting single electron transistor

Nam Kim ^{a,1}, Klavs Hansen ^a, Sorin Paraoanu ^a, Jukka Pekola ^a

^aDepartment of Physics, University of Jyväskylä, P.O.Box 35, FIN-40351 Jyväskylä

Abstract

We have fabricated Nb/(Al-)AlO_x/Nb junctions with a single electron transistor (SET) geometry using conventional e-beam lithographic technique. It was possible to reach a clearly defined superconducting gap of 0.75 meV as measured in the current vs voltage ($I - V$) characteristic curve, which corresponds to T_c of 4.6 K. The Josephson coupling energy was comparable to the charging energy, $E_J \approx E_c = 30 - 40 \mu\text{eV}$.

Key words: Single electron transistor; Josephson effect; Superconductivity

Nb-based single Cooper pair transistor (SCT) has attracted interest [1–3] because of its large superconducting gap. The fact that Nb has a much larger superconducting gap energy than Al [$\Delta_{\text{Nb}}(\text{bulk}) \approx 1.5 \text{ meV}$ and $\Delta_{\text{Al}} \approx 0.2 \text{ meV}$] [4] suggests that the quasiparticle tunneling will be strongly suppressed for Nb-based structures compared with Al-based structures [5]. This is particularly important in the application of these devices as qubits [6] for which quasiparticle contamination is harmful. Thus a demonstration that it is possible to fabricate Nb-based SCT by conventional e-beam lithography is highly significant.

So far two different methods have been adopted to realize Nb-based sub-micron junctions. One is the conventional two angle evaporation technique with a stencil mask consisting of polymethylmethacrylate (PMMA) and co-polymer [P(MMA-MAA)] resist [1]. The Nb film made this way is not of good quality and it was believed this was because it degraded due to contamination from outgassing from the resists during the Nb evaporation. The second method is the multi-layer technique by which Nb/(Al-)AlO_x/Nb tri-layers are formed *in situ*, followed by the fabrication process *ex situ* [2]. In spite of the reliability of the insulating

layer and the high-quality superconducting Nb electrodes, the complicated multi-layer process makes it more difficult to reduce the size of junctions and to align the electrodes on the sub-micron scale than with the self-alignment techniques.

Contrary to the common belief, however, it is possible to manufacture high quality Nb/(Al-)AlO_x/Nb tunnel junctions by the conventional two angle evaporation technique with PMMA and copolymer resist as a mask. Our results were obtained with a standard e-beam lithography process that followed common procedures and conventional recipes [7]. We fabricated Nb/(Al-)AlO_x/Nb junctions with a single electron transistor geometry (see the lower right inset of Fig. 1). The 20 nm thick Al layer was evaporated on the 53 nm thick Nb layer. The Al layer was subsequently oxidized in a static oxygen pressure of 100 mbar for 5 minutes. Then the third layer of Nb was deposited at a different angle.

The $I - V$ curve shown in Fig. 1, recorded at a temperature around 100 mK, clearly shows a superconducting gap, $\Delta_{\text{Nb}} \approx 0.75 \text{ meV}$. This is about 66% of the expected value, assuming T_c of both electrodes and island is 7 K [7]. The suppression of the superconducting gap could be due to the existence of an incompletely oxidized Al layer. For this sample the total film thickness of Nb and a not-fully oxidized Al layer is estimated as 106 nm and at most 20 nm, respectively.

¹ Corresponding author. Present address: Division of Electromagnetic Metrology, Korea Research Institute of Standards and Science, P.O.Box 102, Yuseong, Daejeon 305-600, Republic of Korea. E-mail: namkim@kriss.re.kr

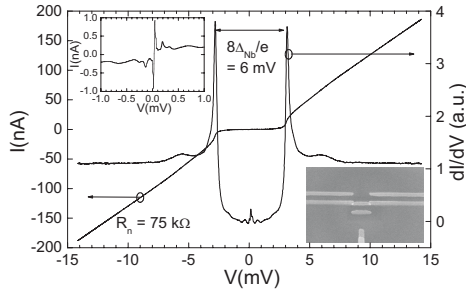


Fig. 1. $I-V$ and $dI/dV-V$ characteristic of Nb/(Al-)AlO_x/Nb junctions at a temperature around 100 mK. $\Delta_{Nb} = 0.75$ meV. Inset (upper left): zoom of the $I-V$ curve around zero bias voltage. Inset (lower right): scanning electron micrograph of the Nb/(Al-)AlO_x/Nb junctions. The width of Nb electrode is about 0.24 μ m.

Figure 2 shows examples of the gate modulated current at a bias voltage $V_b = 0.17$ mV as well as the gate modulated switching current I_{sw} which is measured with the Josephson critical current (see the upper left inset of Fig.1). A comparison of the modulation period with that of a Al/AlO_x/Al SET with the same geometry indicates that the period we observed in Fig. 2 corresponds to e/C_g , where C_g is the gate capacitance. If we use the simple estimate that the crossover temperature T^* below which only $2e$ period appears is $T^* \approx T_c/5$ [5] we have $T^* \approx 900$ mK. The factor $1/5$ pertains to Al but is not expected to be dramatically different for Nb since it is the logarithm of an effective number of electrons. Even though the sample temperature is far below T^* we have not observed the $2e$ periodicity so far. One reason could be the existence of nonequilibrium quasiparticles which have survived in spite of the introduction of quasiparticle traps close to the junctions. Further study on this problem is needed.

For the sample in Fig.1 and 2(a) the Josephson coupling energy $E_J = (\hbar/4e^2) \cdot (\Delta_{Nb}/2R_n) \approx 32$ μ eV was comparable to the charging energy $E_c \approx 35$ μ eV. The latter was determined from the $I-V$ curves as function of gate voltages.

The upper left inset of Fig. 1 shows the $I-V$ curve around zero bias voltage. The magnitude of the critical current is about 1 nA which gives $E_J = \hbar I_c/2e \approx 2$ μ eV which is one order of magnitude smaller than the value from the formula $E_J = (\hbar/4e^2) \cdot (\Delta_{Nb}/2R_n)$. In addition to the critical current peak, a clearly visible second current peak is also observed. It is believed to arise from the resonant tunneling process of Cooper pairs [8]. The Fraunhofer diffraction pattern of the first and the second current peaks, shown in Fig. 2(c), supports this suggestion. The value of λ_L obtained from the scan, 60 nm, is not far from that of λ_L for bulk Nb which is 85 nm [9].

This work has been supported by the Academy of Finland under the Finnish Centre of Excellence Pro-

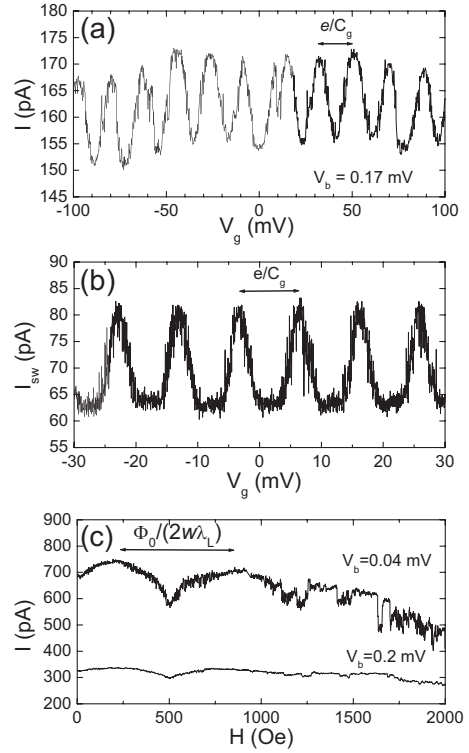


Fig. 2. (a) Gate modulated current with a bias voltage $V_b = 0.17$ mV. (b) Modulation of switching current I_{sw} measured on a sample with different distance between island and gate than sample for (a). (c) Fraunhofer diffraction pattern of the first and the second current peak at $V_b = 0.04$ mV and $V_b = 0.2$ mV, respectively. $\Phi_0 = \hbar/2e$. $w = 0.24$ μ m. λ_L is the London penetration depth.

gramme 2000-2005 (Project No. 44875, Nuclear and Condensed Matter Programme at JYFL) and by the EU (contract IST-1999-10673).

References

- [1] Y. Harada *et al.*, Appl. Phys. Lett. **65** (1994) 636.
- [2] A. B. Pavolotsky *et al.*, J. Vac. Sci. Tech. B **17** (1999) 230; V. Patel and J.E. Lukens, IEEE Trans. Appl. Supercond. **9** (1999) 3247; K. Blüthner *et al.*, J. de Physique IV **6** (1996) C3-163.
- [3] R. Dolata *et al.*, App. Phys. Lett. **80** (2002) 2776.
- [4] C. P. Poole, Jr., H.A. Farach, and R.J. Creswick, *Superconductivity* (Academic Press, New York, 1995) p.61.
- [5] M. Tinkham, *Introduction to Superconductivity* (McGraw-Hill, New York, 1996) p.269.
- [6] D. Vion *et al.*, Science **296** (2002) 886.
- [7] N. Kim *et al.*, J. Vac. Sci. Technol. B. **20** (2002) 386.
- [8] T. A. Fulton *et al.*, Phys. Rev. Lett. **63** (1989) 1307.
- [9] p. 271 in Ref. [7].